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January 21, 2005

Date

Kevin L. Daffer

| APPLICATION NO. | FILING DATE | FIRST NAMES INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/025,511      | 12/19/2001  | Andrey Zagrebelsky   | 5298-07600          | 6370             |

## TITLE OF INVENTION: METHODS FOR POLISHING A SEMICONDUCTOR TOPOGRAPHY

| APPLN. TYPE     | SMALL ENTITY | ISSUE FEE      | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE   |
|-----------------|--------------|----------------|-----------------|------------------|------------|
| nonprovisional  | NO           | \$1,400.00     | \$0.00          | \$1,400.00       | 01/27/2005 |
| EXAMINER        | ART UNIT     | CLASS-SUBCLASS |                 |                  |            |
| Rose, Robert A. | 3723         | 451-041.000    |                 |                  |            |

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1 Kevin L. Daffer

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(A) NAME OF ASSIGNEE: Cypress Semiconductor Corp.

(B) RESIDENCE (CITY &amp; STATE OR COUNTRY): San Jose, CA

Please check the appropriate assignee category indicated below (will not be printed on the patent):  individual  corporation or other private group entity  government

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| (Attorney's Signature)             | (Date)  |  |
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